PNP Silicon General Purpose Amplifier Transistor

This PNP transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416/SC-90 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Reduces Board Space
- High h_{FE}, 210-460 (typical)
- Low V_{CE(sat)}, < 0.5 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

| Rating | Symbol | Value | Unit |
|--------------------------------|----------------------|-------|------|
| Collector – Emitter Voltage | V _{(BR)CBO} | -60 | Vdc |
| Collector – Base Voltage | V _{(BR)CEO} | -50 | Vdc |
| Emitter - Base Voltage | V _{(BR)EBO} | -6.0 | Vdc |
| Collector Current - Continuous | I _C | -100 | mAdc |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|----------------------------|------------------|------------|------|
| Power Dissipation (Note 1) | P _D | 150 | mW |
| Junction Temperature | TJ | 150 | °C |
| Storage Temperature Range | T _{stg} | -55 ~ +150 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

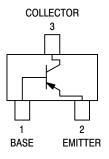


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CASE 463 STYLE 1



MARKING DIAGRAM



= Device Code = Date Code* = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|------------|--------------------|-----------------------|
| 2SA1774G | SC-75 (Pb-Free) | 3,000/Tape & Reel |
| S2SA1774G | SC-75 (Pb-Free) | 3,000/Tape & Reel |
| 2SA1774T1G | SC-75 (Pb-Free) | 3,000/Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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^{1.} Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|----------------------|------|-----|------|------|
| Collector–Base Breakdown Voltage ($I_C = -50 \mu Adc$, $I_E = 0$) | V _{(BR)CBO} | -60 | - | - | V |
| Collector–Emitter Breakdown Voltage $(I_C = -1.0 \text{ mAdc}, I_B = 0)$ | V _{(BR)CEO} | -50 | - | - | V |
| Emitter–Base Breakdown Voltage ($I_E = -50 \mu Adc, I_E = 0$) | V _{(BR)EBO} | -6.0 | - | - | V |
| Collector–Base Cutoff Current $(V_{CB} = -30 \text{ Vdc}, I_E = 0)$ | I _{CBO} | - | - | -0.5 | μΑ |
| Emitter–Base Cutoff Current $(V_{EB} = -5.0 \text{ Vdc}, I_B = 0)$ | I _{EBO} | - | - | -0.5 | μΑ |
| Collector–Emitter Saturation Voltage (Note 2) (I _C = -50 mAdc, I _B = -5.0 mAdc) | V _{CE(sat)} | - | _ | -0.5 | V |
| DC Current Gain (Note 2) $(V_{CE} = -6.0 \text{ Vdc}, I_C = -1.0 \text{ mAdc})$ | h _{FE} | 120 | - | 560 | - |
| Transition Frequency $(V_{CE} = -12 \text{ Vdc}, I_{C} = -2.0 \text{ mAdc}, f = 30 \text{ MHz})$ | f _T | - | 140 | - | MHz |
| Output Capacitance (V _{CB} = -12 Vdc, I _E = 0 Adc, f = 1 MHz) | C _{OB} | | 3.5 | | pF |

^{2.} Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.

TYPICAL ELECTRICAL CHARACTERISTICS

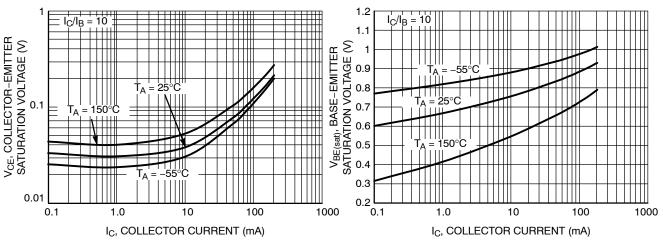


Figure 1. Collector-Emitter Saturation Voltage vs. Collector Current

Figure 2. Base–Emitter Saturation Voltage vs.
Collector Current

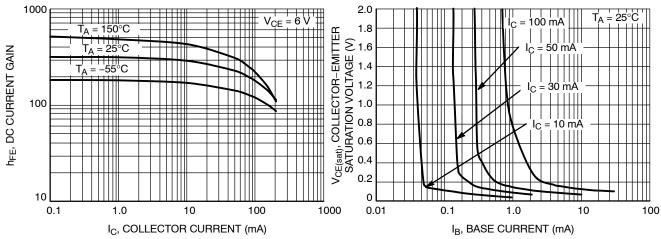


Figure 3. DC Current Gain vs. Collector Current

Figure 4. Saturation Region

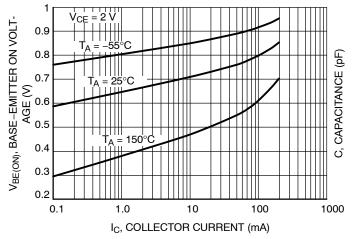


Figure 5. Base-Emitter Turn-ON Voltage vs. Collector Current

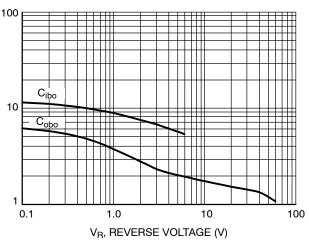


Figure 6. Capacitance

TYPICAL ELECTRICAL CHARACTERISTICS

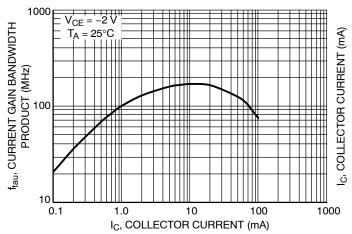


Figure 7. Current Gain Bandwidth Product vs. Collector Current

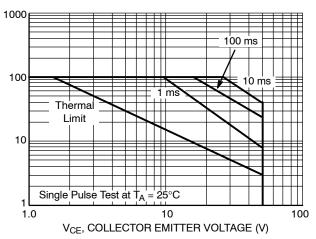


Figure 8. Safe Operating Area

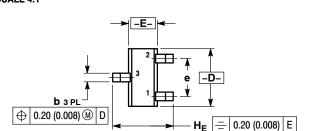
MECHANICAL CASE OUTLINE

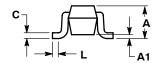




SC-75/SOT-416 CASE 463-01 ISSUE G

DATE 07 AUG 2015





STYLE 1: PIN 1. BASE 2. EMITTER

STYLE 4:

3. COLLECTOR

PIN 1. CATHODE 2. CATHODE 3. ANODE

STYLE 5: PIN 1. GATE 2. SOURCE

3. DRAIN

STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE

STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.

| | | MILLIMETERS | | | INCHES | | |
|-----|---|-------------|------|------|--------|----------|-------|
| DII | И | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | | 0.70 | 0.80 | 0.90 | 0.027 | 0.031 | 0.035 |
| A | 1 | 0.00 | 0.05 | 0.10 | 0.000 | 0.002 | 0.004 |
| b | | 0.15 | 0.20 | 0.30 | 0.006 | 0.008 | 0.012 |
| С | | 0.10 | 0.15 | 0.25 | 0.004 | 0.006 | 0.010 |
| D | | 1.55 | 1.60 | 1.65 | 0.061 | 0.063 | 0.065 |
| E | | 0.70 | 0.80 | 0.90 | 0.027 | 0.031 | 0.035 |
| е | | 1.00 BSC | | | C | 0.04 BSC |) |
| L | | 0.10 | 0.15 | 0.20 | 0.004 | 0.006 | 0.008 |
| HE | : | 1.50 | 1.60 | 1.70 | 0.060 | 0.063 | 0.067 |

GENERIC MARKING DIAGRAM*

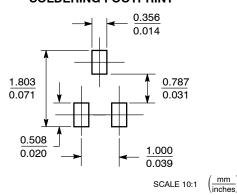


XX= Specific Device Code

Μ = Date Code

= Pb-Free Package

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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|------------------|---------------|--|-------------|--|
| DESCRIPTION: | SC-75/SOT-416 | | PAGE 1 OF 1 | |

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^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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